

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shmuel Wimer
Title: ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

Docket No.: 80107.072US1
Filed: June 29, 2001
Examiner: Dwin M Craig

Serial No.: 09/893,527
Due Date: August 30, 2005
Group Art Unit: 2123

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard and this transmittal document.
☒ An Amendment and Response (8 Pages).

Customer Number 45445:

By Dana B LeMoine
Dana B. LeMoine
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DBL/clh

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11 day of July, 2005.

Chris Hammond

Name

Chris Hammond

Signature

(GENERAL)

S/N 09/893,527

PATENT

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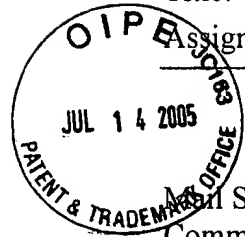
Title: ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

Assignee: Intel Corporation

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

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This responds to the Final Office Action mailed on June 30, 2005. Please amend the above-identified patent application as follows.



AF
JGW